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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/684,993	10/14/2003	Owen T. Richard	200208305-1	4755

22879 7590 09/19/2006

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EXAMINER	
CARPIO, IVAN HERNAN	

ART UNIT	PAPER NUMBER
2841	

DATE MAILED: 09/19/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/684,993	RICHARD ET AL.	
	Examiner	Art Unit	
	Ivan H. Carpio	2841	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

1)☒ Responsive to communication(s) filed on *19 June 2006*.

2a)☒ This action is **FINAL**. 2b)☐ This action is non-final.

3)☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

4)☒ Claim(s) *1-22* is/are pending in the application.

4a) Of the above claim(s) _____ is/are withdrawn from consideration.

5)☐ Claim(s) _____ is/are allowed.

6)☒ Claim(s) *1-22* is/are rejected.

7)☐ Claim(s) _____ is/are objected to.

8)☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

9)☐ The specification is objected to by the Examiner.

10)☐ The drawing(s) filed on _____ is/are: a)☐ accepted or b)☐ objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

12)☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a)☐ All b)☐ Some * c)☐ None of:

1.☐ Certified copies of the priority documents have been received.

2.☐ Certified copies of the priority documents have been received in Application No. _____.

3.☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

1) ☐ Notice of References Cited (PTO-892)
2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3) ☐ Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date _____.
4) ☐ Interview Summary (PTO-413) Paper No(s)/Mail Date. _____ .
5) ☐ Notice of Informal Patent Application
6) ☐ Other: _____.

DETAILED ACTION

Response to Arguments

Applicant's arguments filed 6/19/06 have been fully considered but they are not persuasive. With respect to independent claims 1, 8 and 15 the applicant argues that Davis does not disclose "a chassis support having at least one keyhole, the keyhole adapted to receive a mounting post coupled to a printed circuit board assembly" and where "the chassis support further has at least one guide adapted to align the mounting post with the keyhole", examiner respectfully disagrees. Fig. 9 clearly shows a chassis (104), a keyhole (left part 108) adapted to receive mounting post coupled to a printed circuit board and a guide (the angled edge of slot 108) adapted to align the mounting post with the keyhole. The left end of the slot can be read as the keyhole because that is where the mounting posts rests when the circuit board is mounted properly, furthermore the angled edges of 108 physically guide mounting posts to its final stable position on the chassis.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

> A person shall be entitled to a patent unless –
>
> (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-22 are rejected under 35 U.S.C. 102(b) as being anticipated by Davis (US 6377445).

With respect to claim 1 Davis teaches a printed circuit board assembly mounting system (Fig. 9), comprising: a chassis support (Fig. 9, element 104) having at least one keyhole (Fig. 9, the left end of slot 108, or Fig. 6, element 114), the keyhole adapted to receive a mounting post (Fig. 9, element 162) coupled to a printed circuit board (Fig. 9, element 106) assembly, the chassis support further having at least one guide (Fig. 6, the angled edge of slot 108) adapted to align the mounting post with the keyhole, the mounting post adapted to slidably engage the keyhole to secure the printed circuit board assembly to the chassis support.

With respect to claim 2 and with all the limitations of claim 1, Davis teaches that the guide comprises integrally formed tabs (Fig. 6, the tab protrusions of the angled edge of slot 108) of the mounting support.

With respect to claim 3 and with all the limitations of claim 1, Davis teaches that the guide comprises at least one pair of oppositely facing tabs (Fig. 6, the tab protrusions of the angled edge of slot 108).

With respect to claim 4 and with all the limitations of claim 1, Davis teaches that the at least one pair of tabs (Fig. 6, the tab protrusions of the angled edge of slot 108) disposed spaced apart from each other corresponding to a lateral dimension of the printed circuit board assembly.

With respect to claim 5 and with all limitations of claim 1, Davis teaches a grounding element (Fig. 9, element 170) adapted to be coupled to the mounting post.

With respect to claim 6 and with all the limitations of claim1, Davis teaches a grounding element (Fig. 9, element 164) extending from the printed circuit board assembly to the chassis support.

With respect to claim 7 and with all the limitations of claim 1, Davis teaches that the guide is adapted to restrict lateral movement of the printed circuit board assembly.

With respect to claim 8 Davis teaches a printed circuit board assembly mounting system, comprising: a mounting post (Fig. 9, element 162) coupled to a printed circuit board (Fig. 9, element 106) assembly; and a computer chassis (Fig. 9, element 104) having at least one support member, the support member having a keyhole (Fig. 9, the left end of slot 108, or Fig. 6, element 114) adapted to receive the mounting post, the support member further having at least one guide (Fig. 6, the angled edge of slot 108) adapted to align the mounting post with the keyhole, the mounting post adapted to slidably engage the keyhole to secure the printed circuit board assembly to the support member.

Claims 9-14 are rejected under 102(b) as corresponding to claims 2-7 respectively.

With respect to claim 15 Davis teaches a printed circuit board assembly mounting system, comprising: a printed circuit board assembly (Fig. 9); a chassis support member (Fig. 9, element 104); means for releasably coupling (Fig. 9, element 162) the printed circuit board assembly to the chassis support member; means formed in the chassis support member for enabling slidable engagement (Fig. 9, the left end of slot 108, or Fig. 6, element 114) of the coupling means with the chassis support

member; and means for aligning (Fig. 6, the angled edge of slot 108) the coupling means with the means for enabling slidable engagement.

With respect to claim 16 and with all the limitations of claim 15, Davis teaches that the means for aligning comprises means for restricting lateral movement (Fig. 6, the tab protrusions of the angled edge of slot 108) of the printed circuit board assembly.

With respect to claim 17 and with all the limitations of claim 15, Davis teaches grounding means (Fig. 9, element 164) coupled to the coupling means.

With respect to claim 18 and with all the limitations of claim 15, Davis teaches a plurality of tabs (Fig. 6, the tab protrusions of the angled edge of slot 108) having a lateral spacing corresponding to a lateral dimension of the printed circuit board assembly.

With respect to claim 19 and with all the limitations of claim 15, Davis teaches means for aligning (Fig. 6, the tab protrusions of the angled edge of slot 108) comprises at least one pair of tabs.

With respect to claim 20 Davis teaches a printed circuit board assembly system, comprising a chassis support (Fig. 9, element 104) having at least one keyhole (Fig. 9, left end of element 108), they keyhole adapted to receive a mounting post (Fig. 9, element 162) coupled to a printed circuit board assembly (Fig. 9, element 106), the chassis support further having at least one guide (Fig. 6, the angled edge of slot 108) configured to engage at least a portion of the printed circuit board assembly to align

the mounting post with the keyhole, the mounting post adapted to slidably engage the keyhole to secure the printed circuit board assembly to the chassis support.

With respect to claim 21 and with all the limitations of claim 20, Davis teaches that the guide comprises integrally formed tabs (Fig. 6, the angled edges of element 108) of the mounting support.

With respect to claim 22 and with all the limitations of claim 20, Davis teaches that the guide comprises at least one pair of oppositely facing tabs (Fig. 6, the angled edges of element 108).

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ivan H. Carpio whose telephone number is 571-272-8396. The examiner can normally be reached on M-R 6:00am - 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammie Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

IC

DEAN A. REICHARD
SUPERVISORY PATENT EXAMINER
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9/15/06